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Morrow et. al.
YOR 9-2000-0472 US1 (RPT) (8728-416)

100

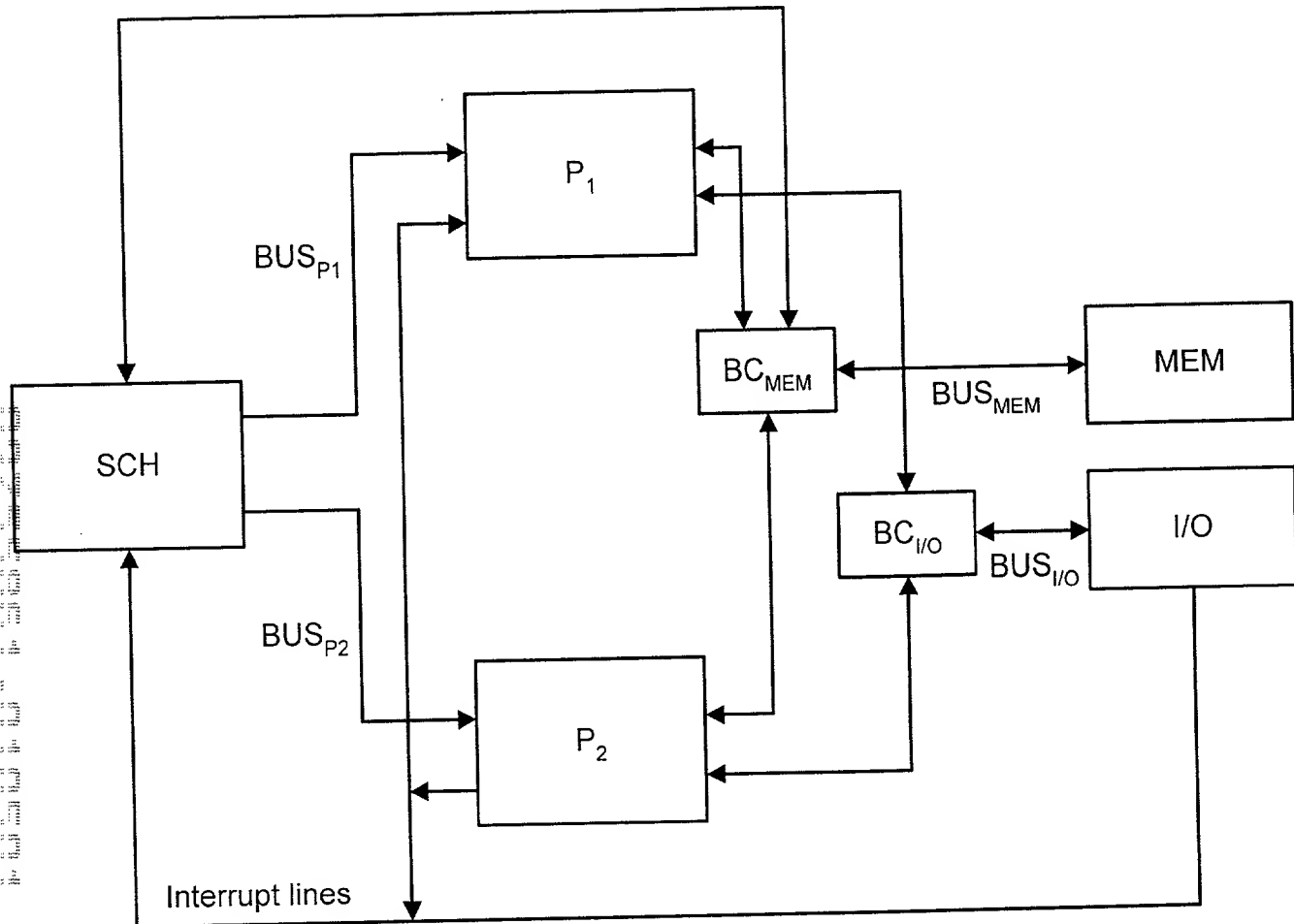


FIG. 1

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<i>Parameter</i>	<i>Type</i>	<i>Unit</i>	<i>Description</i>
TID	word	n/a	Task IDentification number
NP	byte	n/a	Number of potential host processing units
P _{TID,1}	byte	n/a	ID of most energy efficient processor
CPS _{TID,P_{TID,1}}	word	[kHz]	Required cycles/sec to sustain task
ADDR ₁	void*	n/a	Pointer to task code
.			
.			
.			
P _{TID,NP}	byte	n/a	ID of least energy efficient processor
CPS _{TID,P_{TID,NP}}	word	[kHz]	Required cycles/sec to sustain task
ADDR _{NP}	void*	n/a	Pointer to task code

FIG. 2

<i>Parameter</i>	<i>Type</i>	<i>Unit</i>	<i>Description</i>
N	byte	n/a	Number of processing units
CPS ₁	word	[kHz]	Currently available cycles/sec
.			
.			
.			
CPS _{NP}	word	[kHz]	Currently available cycles/sec

FIG. 3

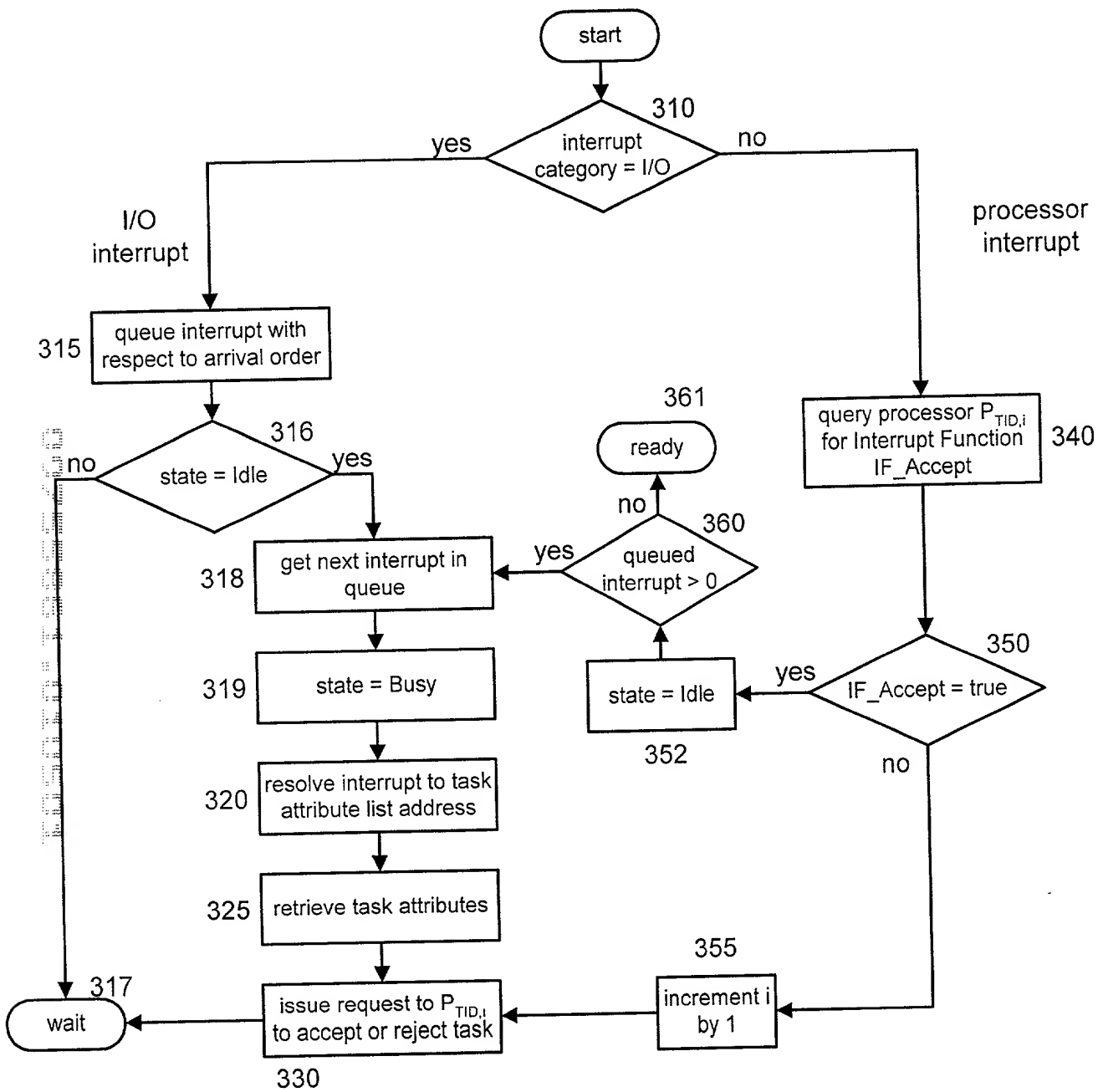


FIG. 4

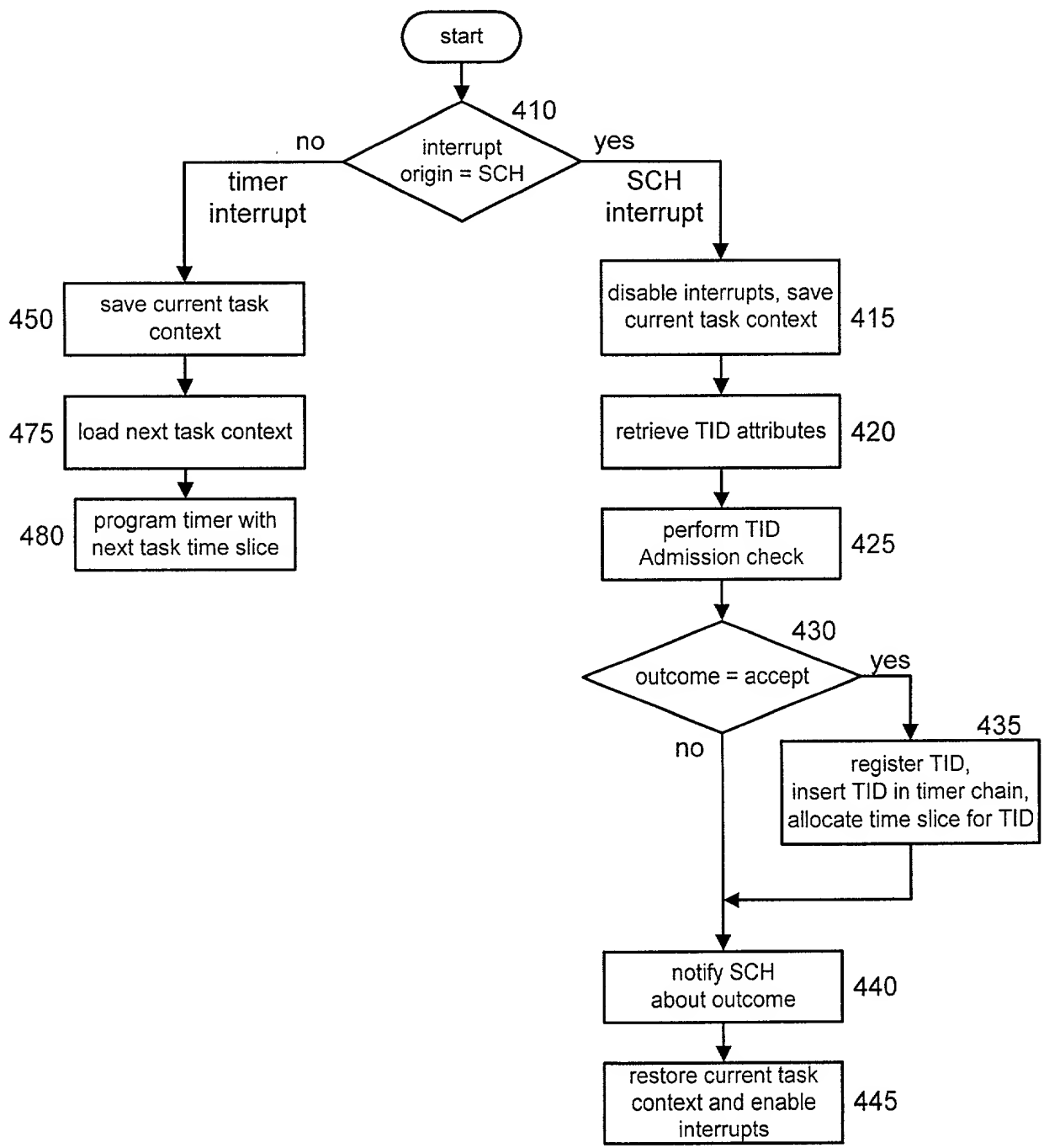


FIG. 5